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	<b>Filing Date</b>	
	<b>First Named Inventor</b>	VAN DER PLOEG, HENDRIK
	<b>Group Art Unit</b>	2819
	<b>Examiner Name</b>	Khai M. Nguyen
	<b>Atty. Docket Number</b>	NL03 1246 US1

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number No.-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns Lines, Where Relevant Passages or Relevant Figures Appear
/KN/	1	US- 6 486 820	11/26/2002	ALLWORTH PETER C ET CL.	
/KN/	2	US- 5 739 781	04/14/1998	KAGEY MARK	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Document Number (ctry <sup>3</sup> -no.-kind <sup>5</sup> , if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of cited document	Pages, Columns Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>

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/KN/	1	THOMAS B. CHO: "A 10 b, 20 MSAMPLE/S, 35 MW PIPELINE A/D CONVERTER." IEEE JOURNAL OF SOLID-STATE CIRCUITS; VOL 30, NO. 3; MARCH 1995; PP 166-172.				
/KN/	2	OPRIS ION E. ET AL: "A SINGLE-ENDED 12-BIT . . . "; IEEE JOURNAL OF SOLID-STATE CIRCUITS; VOL. 33, NO. 12; DEC. 1998. PP 1893-1903				
/KN/	3	INGINO JOSEPH ET AL. : "A CONTINUOUSLY CALIBRATED 12-B . . . " IEEE JOURNAL OF SOLID-STATE CIRCUITS; VOL 33, NO. 12; DEC. 1998; PP 1920-1931.				
/KN/	4	YANG WENHUA ET AL: "A 3-V 340-MW 14-B 75-MSAMPLE/S . . . "; IEEE JOURNAL OF SOLID STATE CIRCUITS; VOL. 36; NO. 12; DEC. 2001; PP 1931-1936				
/KN/	5	YU PAUL ET AL.: A 14b 40MSAMPLE/S PIPLINED. . . " ISSCC 2001 / SESSION 8 / NYQUIST ADCS / 8.6; IEEE INTERNATIONAL SOLID STATE CIRCUITS CONFERENCE; 2001; DALLAS TX; PP 136-137, 439-440.				
/KN/	6	MANGELSDORF CHRIS ET AL. : "A TWO RESIDUE ARCHITECTURE . . . "; ISSCC 93 / SESSION 4 / DATA CONVERSION / PAPER WP 4.4; IEEE INTERNATIONAL SOLID-STATE CIRCUITS CONFERENCE; PP 64-65				

<b>Examiner Signature</b>	/Khai Nguyen/	<b>Date Considered</b>	08/01/2007
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